

IN THE CLAIMS

1-17. (canceled)

18. (currently amended) A run capacitor/positive temperature coefficient resistor/overload (CAP/PTCR/OL) assembly cover configured to couple to a PTCR/OL base comprising:

a first surface;

a plurality of sidewalls that extend from said first surface and are integrally formed with said first surface, said sidewalls extend from said first surface and form a compartment that is sized to at least partially receive a run capacitor ~~assembly~~ therein;

at least one aperture extending through said cover internal to said compartment;

a second surface, opposite to said first surface; and

a plurality of projections extending from said second surface.

19. (original) A cover in accordance with Claim 18 wherein said cover sidewalls extend substantially perpendicularly from said cover first surface.

20. (currently amended) A cover in accordance with Claim 18 wherein ~~said run capacitor assembly comprising~~ at the run capacitor and at least one electrical terminal ~~is positioned~~ are positioned within said cover compartment such that said terminal extends through said aperture.

21. (original) A cover in accordance with Claim 20 wherein said run capacitor assembly is potted in said cover compartment.

22. (original) A cover in accordance with Claim 18 wherein said cover further comprises at least one aperture extending through said cover external to said cover compartment to receive at least one electrical terminal assembly.

23. (original) A cover in accordance with Claim 22 wherein said cover further comprises a wall integrally formed with said cover first surface, said wall circumscribing said aperture.

24. (original) A cover in accordance with Claim 18 wherein a portion of said cover extends into said cover compartment.

25. (original) A cover in accordance with Claim 18 wherein said plurality of projections are configured to extend into a base compartment to engage a plurality of components disposed internally in the base compartment.

26. (currently amended) A run capacitor/positive temperature coefficient resistor/overload (CAP/PTCR/OL) assembly cover configured to couple to a PTCR/OL base comprising:

a first surface;

a plurality of sidewalls that extend from said first surface and are integrally formed with said first surface, said sidewalls extend from said first surface and form a platform that is sized to at least partially receive a run capacitor assembly thereon, wherein said cover is configured to couple to an enclosure configured to enclose at least a portion of the run capacitor;

at least one aperture extending through said platform;

a second surface, opposite to said first surface; and

a plurality of projections extending from said second surface.

27. (original) A cover in accordance with Claim 26 wherein said cover sidewalls extend substantially perpendicularly from said cover first surface.

28. (currently amended) A cover in accordance with Claim 26 wherein ~~said run capacitor assembly comprising a~~ the run capacitor and at least one electrical terminal ~~is positioned~~ are positioned within said platform such that said terminal extends through said aperture.

29. (currently amended) A cover in accordance with Claim 28 wherein ~~an enclosure couples to said cover eircumscribing~~circumscribes said platform ~~and enclosing said run capacitor assembly~~, said enclosure filled by a potting material thereby encapsulating said run capacitor assembly.

30. (original) A cover in accordance with Claim 26 wherein said cover further comprises at least one aperture extending through said cover external to said platform to receive at least one electrical terminal assembly.

31. (original) A cover in accordance with Claim 30 wherein said cover further comprises a wall integrally formed with said cover first surface, said wall circumscribing said aperture.

32. (original) A cover in accordance with Claim 26 wherein said plurality of projections are configured to extend into a base compartment to engage a plurality of components disposed internally in the base compartment.

33. (new) A cover in accordance with Claim 18 wherein the cover compartment is configured to receive the run capacitor.